Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	34	arrangment and (load\$1 or lamp\$1 or light\$1) and ((switch\$3 or transistor\$1) same inductor)same diode	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:06
L2	17107	(load\$1 or lamp\$1 or light\$1) and ((switch\$3 or transistor\$1) same inductor)same diode	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:06
L3 .	2453	315/307.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:06
L4	3692	315/291.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:06
L5	958	315/244.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:06
L6	2264	315/224.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:07
L7	327	315/320.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:13
L8	71	315/321.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:13

L9	368	315/322.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:14
L10	10834	l2 and @pd<="20030123"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 13:00
L11	337	I10 and I3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR .	ON	2007/12/25 11:25
L12	244	I10 and I4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:15
L13	129	I10 and I5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:15
L14	291	110 and 16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:15
L15	6	110 and 17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:15
L16	0	110 and 18	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:15

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L17	2	substantially near4 zero near4 voltage and I1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:26
L18	335	substantially near4 zero near4 voltage and I10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 12:47
L19	. 13	I18 and I3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:27
L20	17	118 and 14	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON .	2007/12/25 11:26
L21	6	I18 and I5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:26
L22	11	I18 and I6	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:26
L23	0	l18 and l7	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:26
L24	0	l18 and l8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 11:26

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L25	6140	I10 and zero	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR ,	ON	2007/12/25 13:01
L26	3307	I10 and zero with (switch\$3 or transistor\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 13:01
L27	· 89714	"315"/\$7.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 13:01
L28	526	126 and 127	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 13:05
L29	195	(diode or voltage) with open\$3 and 128	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 14:52
L30	135216	I29 amd mosfet\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/12/25 14:52
L31	61	l29 and mosfet\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR ·	ON	2007/12/25 14:52